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FACSIMILE COVER SHEET

To: Commissioner for Patents

PO Box 1450

Alexandria, CA 22313-1450 Facsimile No. (703) 872-9306

Re: Application No. 10/821,505

U.S. Non-Provisional Patent Application for

METHOD AND SYSTEM FOR PROVIDING FAST DESIGN FOR TESTABILITY

PROTOTYPING IN INTEGRATED CIRCUIT DESIGNS

Inventor: Steve C. Huang et al Filing Date: April 9, 2004 Our File No. ID-04-01

From: Kuni Oh, Esq.

JACKSON & CO., LLP 6114 La Salle Ave., # 507 Oakland, CA 94611-2802 United States of America Tel (510) 652-6418 Fax (510) 652-5691

Pages:42 (Including Cover Sheet)	Date: <u>April 27, 2005</u>
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of) Group Art Unit: 3612
Steve C. HUANG et al	Examiner: Not yet assigned
Appln. No. 10/821,505) INFORMATION DISCLOSURE STATEMENT) TRANSMITTAL
Filed: April 9, 2004	.)
For: METHOD AND SYSTEM FOR PROVIDING FAST DESIGN FOR) Customer No. 30349
TESTABILITY PROTOTYPING IN) Jackson & Co., LLP
INTEGRATED CIRCUIT DESIGNS) 6114 La Salle Ave., # 507
	Oakland, CA 94611-2082

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Sir: Enclosed herewith please find the following papers:

- 1. Information Disclosure Statement Transmittal (in duplicate);
- 2. Information Disclosure Statement;
- 3. PTO/SB/08B Form
- 4. Certificate of Transmission (in duplicate);
- 5. Deposit Account Authorization (in duplicate).

The Commissioner is hereby authorized to charge any additional fees (or credit any overpayment) associated with this communication and which may be required under 37 CFR §1.16 or §1.17 to Deposit Account No. 50-2019. <u>A duplicate of this transmittal is ettached.</u>

Respectfully submitted, JACKSON & CO., LLP

April 27, 2005

(Date)

(ID-04-01)

By:

Seong-Run Oh

Registration No. 48,210 Attorneys for Applicant(s)

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April 27, 2005 (Date)

(ID-04-01)

By:

Seong-Kun Oh
Registration No. 48,210
Attorneys for Applicant(s)

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PATENT

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in re i	Patent Application of)	Group Art Unit: 3612
Steve C. HUANG et al			Examiner:
Appli	cation No. 10/821,505)	INFORMATION DISCLOSURE STATEMENT
Filed:	April 9, 2004)	Z.435 = 7.134 1 2
For:	METHOD AND SYSTEM FOR)	Jackson & Co., LLP
	PROVIDING FAST DESIGN FOR)	6114 La Salle Ave., #507
	TESTABILITY PROTOTYPING IN)	Oakland, CA 94611-2802
•	INTEGRATED CIRCUIT DESIGNS)	
			Customer No. 30349

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Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir: Applicant submit herewith patents, publications or other information, attached hereto and listed on the attached Form PTO-1449 (modified), of which aware, which believe may be material to the examination of this application and in respect of which there may be a duty to disclose in accordance with 37 CFR § 1.56.

This Information Disclosure Statement:

- (a) [] accompanies the new patent application submitted herewith. 37 CFR § 1.97(a).
- (b) [] is filed within three months after the filing date of the application or within three months after the date of entry of the national stage of a PCT application as set forth in 37 CFR § 1.491.

-2-

- (c) [X] as far as is known to the undersigned, is filed before the mailing date of a first Office Action on the merits.
- (d) [] is filed after the first office action and more than three months after the application's filing date or PCT national stage date of entryfiling but, as far as is known to the undersigned, prior to the mailing date of either a final rejection or a notice of allowance, whichever occurs first, and is accompanied by either the fee (\$240) set forth in 37 CFR § 1.17(p) or a certification as specified in 37 CFR § 1.97(e), as checked below.
- (e) [] is filed after the mailing date of either a final rejection or a notice of allowance, whichever occurred first, and the Issue Fee has not been paid, and is accompanied by the fee (\$130) set forth in 37 CFR § 1.17(i)(1) and a certification as specified in 37 CFR § 1.97(e), as checked below. This document is to be considered as a petition requesting consideration of the information disclosure statement.

[If either of boxes (d) or (e) are checked above, the following "certification" under 37 CFR § 1.97(e) may need to be completed.] The undersigned certifies that:

- **(f)** Π Each item of information contained in the information disclosure statement was cited in a communication mailed from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this information disclosure statement.
- No item of information contained in this information Π (g) disclosure statement was cited in a communication mailed from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned after making reasonable inquiry, was known to any individual designated in 37 CFR § 1.56(c) more than three months prior to the filing of this information disclosure statement.

A list of the patent(s) or publication(s) is set forth on the attached Form

-3-

PTO/SB/08B.

A copy of the items on PTO/SB/08B is supplied herewith:

(h) [X] each (i) [] none (j) [] only those listed below:

Those patent(s) or publication(s) which are marked with an asterisk (*) in the attached form PTO/SB/08B are not supplied because they were previously cited by or submitted to the Office in a prior application no., filed and relied upon in this application for an earlier filing date under 35 U.S.C. § 120, or because pursuant to Official Gazette dated August 5, 2003, the Patent Office waives the requirement under 37 CFR 1.98(a)(2)(i) for submitting a copy of each cited US patent and each US patent application publication for all US national patent applications filed after June 30, 2003, and for all internatonal application that have entered the national stage under 35 USC 371 after June 30, 2003. See 37 CFR 1.491(b).

A concise explanation of relevance of the items listed on form PTO/SB/08B is:

- (k) [X] not given
- (1) [] given for each listed item
- (m) [] given for only non-English language listed item(s) [Required]
- (n) [] is in the form of an English language copy of a Search Report from a foreign patent office, issued in a counterpart application, which refers to the relevant portions of the references [copy attached].

The Examiner is reminded that a "concise explanation of the relevance" of the submitted items "may be nothing more than identification of the particular figure or paragraph of the patent or publication which has some relation to the claimed invention," MPEP § 609.

-4-

While the information and references disclosed in this Information

Disclosure Statement may be "material" pursuant to 37 CFR § 1.56, it is not
intended to constitute an admission that any patent, publication or other
information referred to therein is "prior art" for this invention unless specifically
designated as such.

In accordance with 37 CFR § 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 CFR § 1.56(a) exists. It is submitted that the Information Disclosure Statement is in compliance with 37 CFR § 1.98 and MPEP § 609 and the Examiner is respectfully requested to consider the listed references.

[X] The Commissioner is hereby authorized to charge our Deposit Account
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Respectfully submitted,
JACKSON & CO., L.L.P.

Dated: April 27, 2005

Seong-Kun Oh

Reg. No. 48,210 Attorneys for Applicant

Our File No. ID-04-01

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Respectfully submitted,

JACKSON & CO., L.L.P.

Dated: April 27, 2005

Seong-Kun Oh

Reg. No. 48,210

Attorneys for Applicant

Our File No. ID-04-01

PTO/S8/08B (08-03)
Approved for use through 07/31/2008. OMB 0851-0031
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

Substitute for form 1449/PTO		re required to respond to a collection of information unless it contains a valid QMB control number. Complete if Known			
			Application Number	10/821,505	_
INFORMAT	ION DIS	CLOSURE	Filing Date	April 9, 2004	_
STATEMENT BY APPLICANT		First Named Inventor	Steve C. Huang	_	
(Use as many sheets as necessary)			Art Unit	3612	_
(USO as Many suitcus as nacessary)		Examiner Name		_	
Sheet one	of	one	Attomey Docket Number	ID-04-01	J

		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
		DEREK L BEATTY et al., Formally Verifying A Microprocessor Using a Simulation Methodology, 1994, 31st ACM/IEEE Design Automation Conference, pp. 596-602	
		ALFRED KOLBL et al., Symbolic RTL Simulation, 2001, DAC 2001, pp.47-52	
		SRIRAM C. KRISHNAN et al., Symbolic Simulatino Formally Verifies ECC, April 2, 2002, EEdesign.	
		ACCULOGIC Web page, Boundary Scan, 2003.	
		JTAG Technologies Web page, Unlocking the power of Boundary-scan, 2001.	
		MARCUS HEDLUND, The Standard for Embedded Core Test, 7 pages, Unversity of Jonkoping, School of Engineering, Sweden	

I Examiner I	Date
	1 I
Signature	Considered
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^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered, Include copy of this form with next communication to applicant.

If you need assistance in completing the form, call 1-800-PTO-9199 (1-800-786-9199) and select option 2.

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Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.

This collection of Information is required by 37 CFR 1.98. The Information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chtel Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.